Fig. 12

Access Control Operation with Request Routing

```
┌─────────────────────────────────────────┐
│ host interface adapter receives request  │
│ from host, which specifies host ID and   │ ╲ 702
│ logical storage device (LSD) ID          │
└─────────────────────────────────────────┘
                    │
                    ▼
┌─────────────────────────────────────────┐
│ host interface adapter maps external LSD │
│ ID received in host request to internal  │ ╲ 704
│ LSD ID                                   │
└─────────────────────────────────────────┘
                    │
                    ▼
┌─────────────────────────────────────────┐
│ host interface adapter looks up LSD ID   │
│ in access table to determine if host has │ ╲ 706
│ access to specified logical storage      │
│ device                                   │
└─────────────────────────────────────────┘
                    │
                    ▼
              ◇ access         no     ┌──────────────────────────────┐
              ◇ permitted? ◇ ────────▶│ host interface adapter        │
                    ╲ 708              │ transmits response to host    │ ╲ 712
                    │                  │ indicating access is denied   │
                   yes                 └──────────────────────────────┘
                    │
                    ▼
┌─────────────────────────────────────────┐
│ host interface adapter determines which  │
│ microprocessor is designated to process  │ ╲ 1202
│ requests for the LSD                     │
└─────────────────────────────────────────┘
                    │
                    ▼
┌─────────────────────────────────────────┐
│ host interface adapter routes request to │
│ designated microprocessor as determined  │ ╲ 1204
│ at block 1202                            │
└─────────────────────────────────────────┘
                    │
                    ▼
┌─────────────────────────────────────────┐
│      microprocessor processes request    │ ╲ 716
└─────────────────────────────────────────┘
```